

Fig. 1

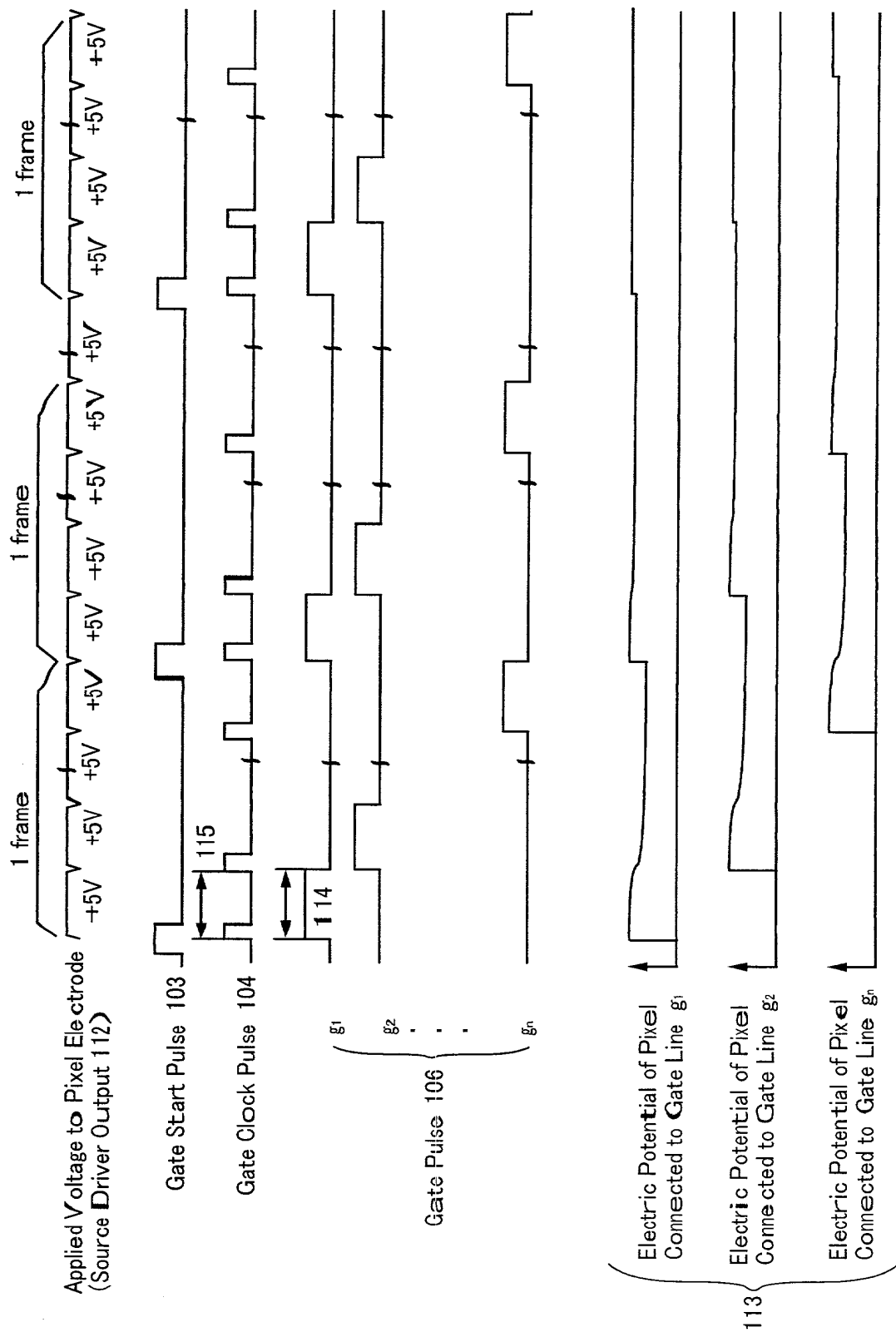


Fig. 2

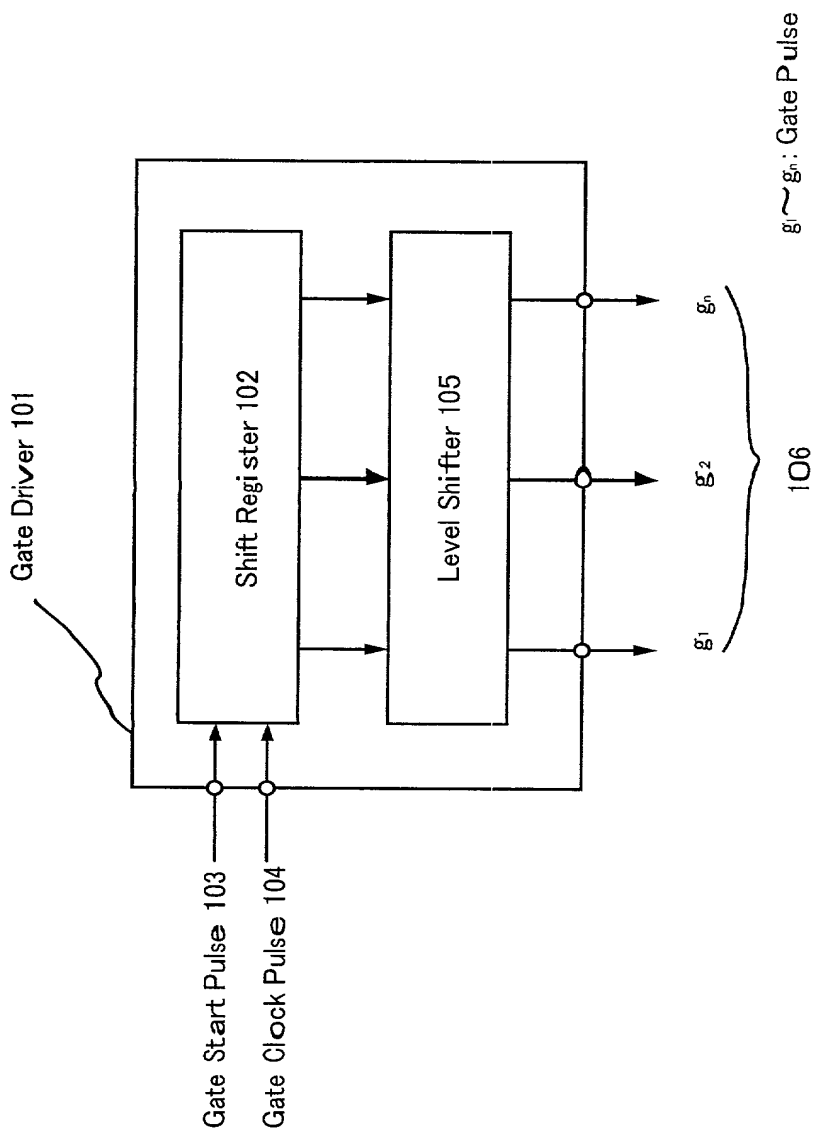


Fig. 3

100F50'02145860

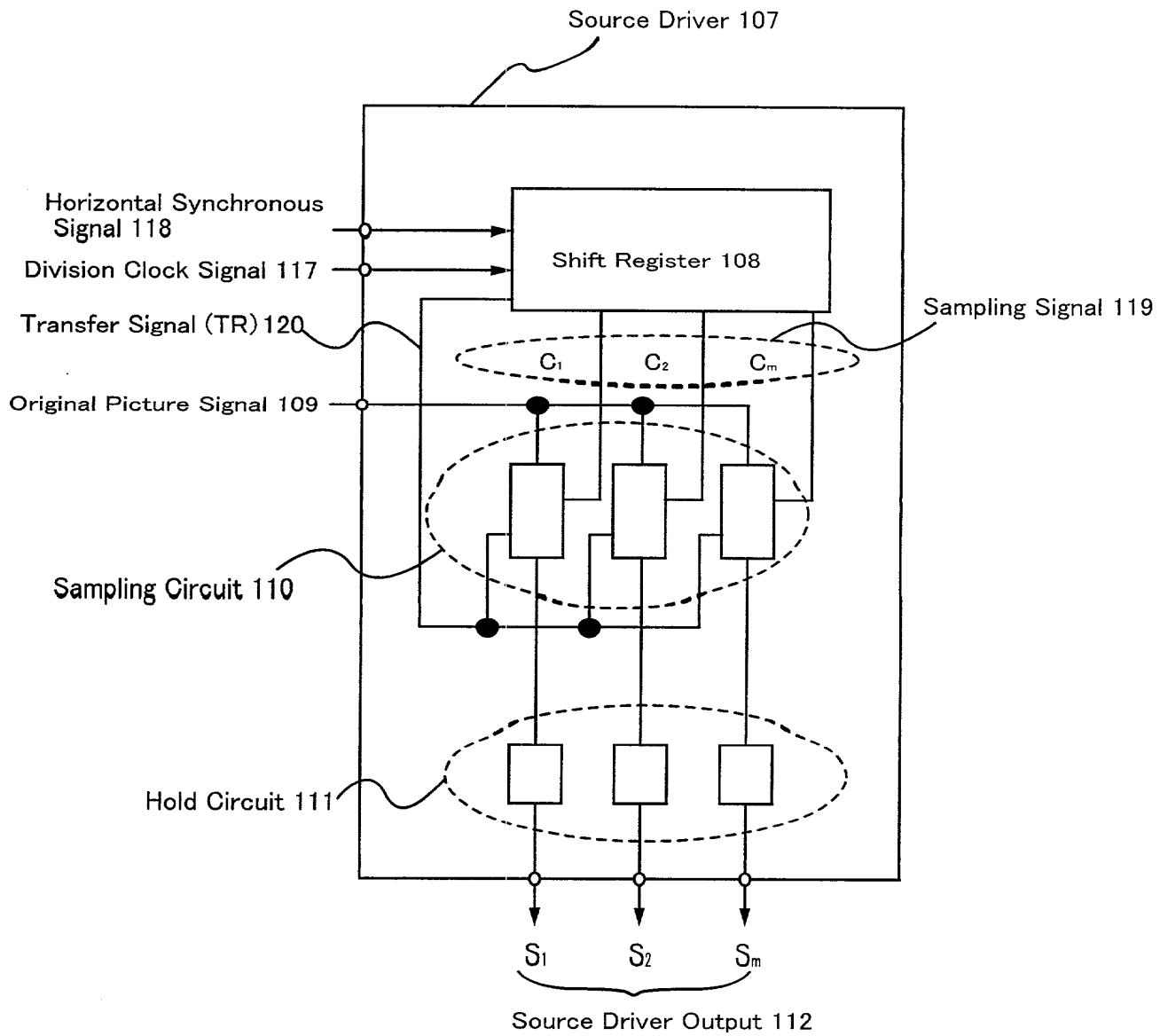


Fig. 4

0954.20.051004
100150.0274560

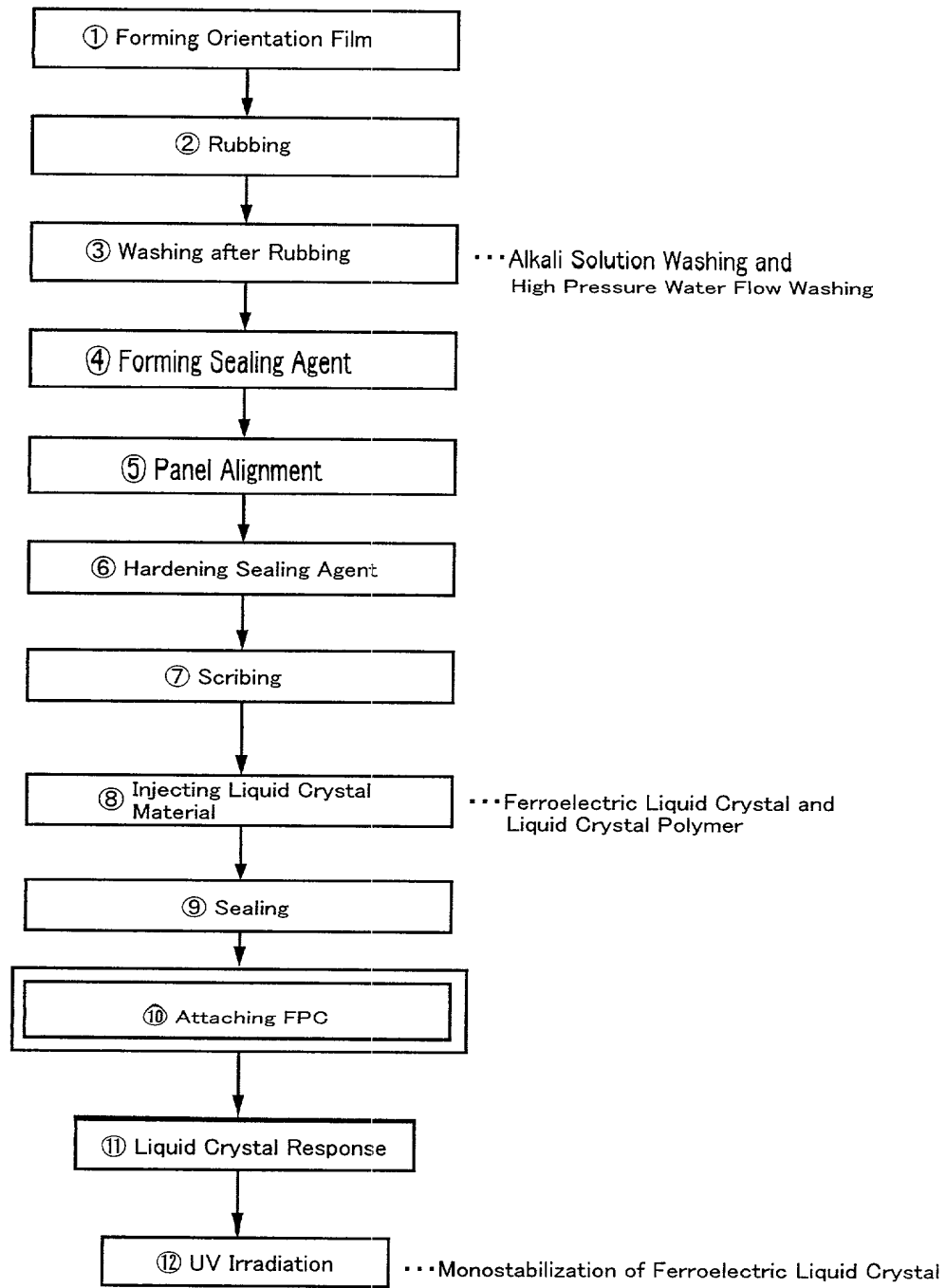


Fig. 5

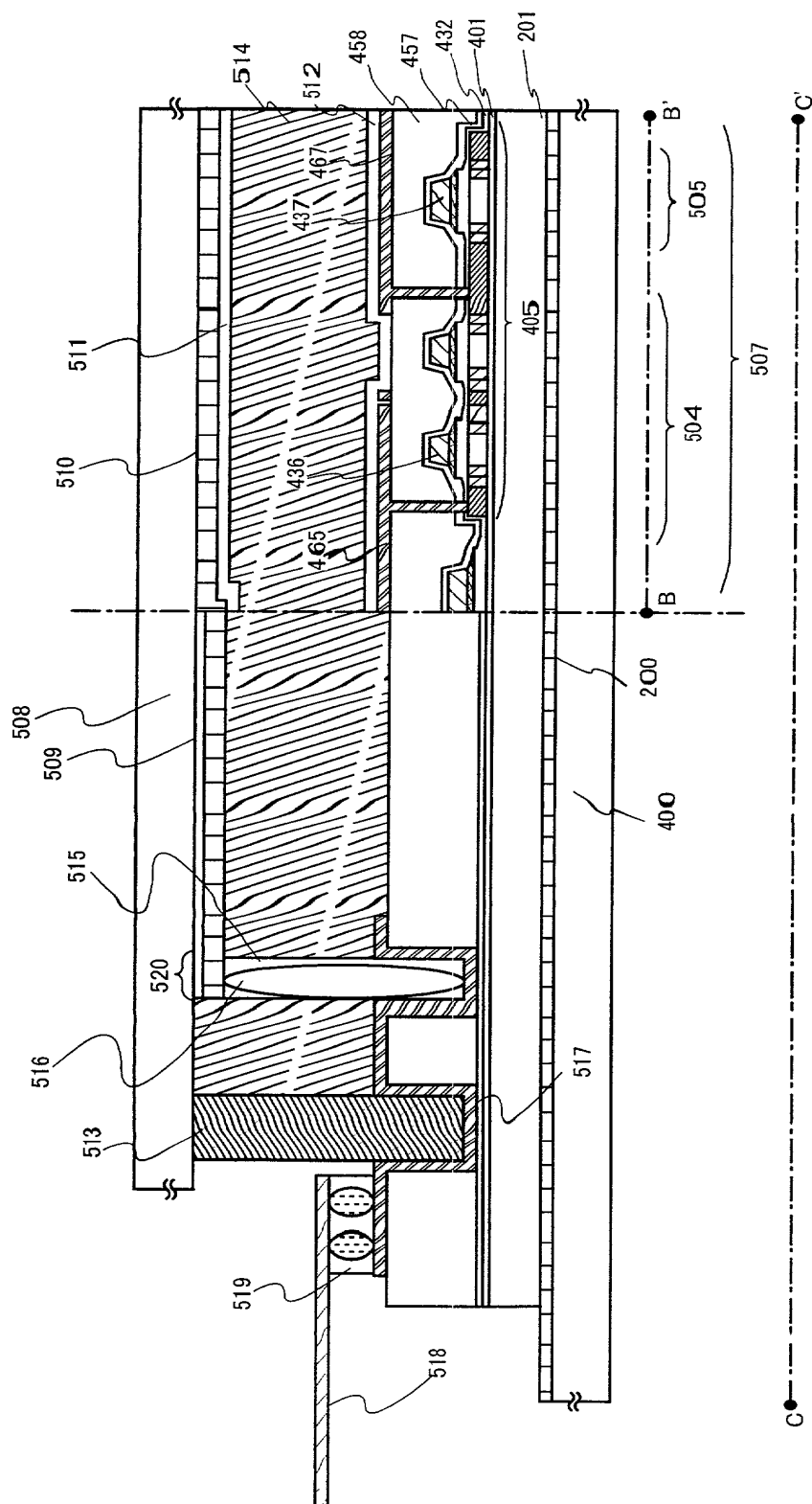


Fig. 6

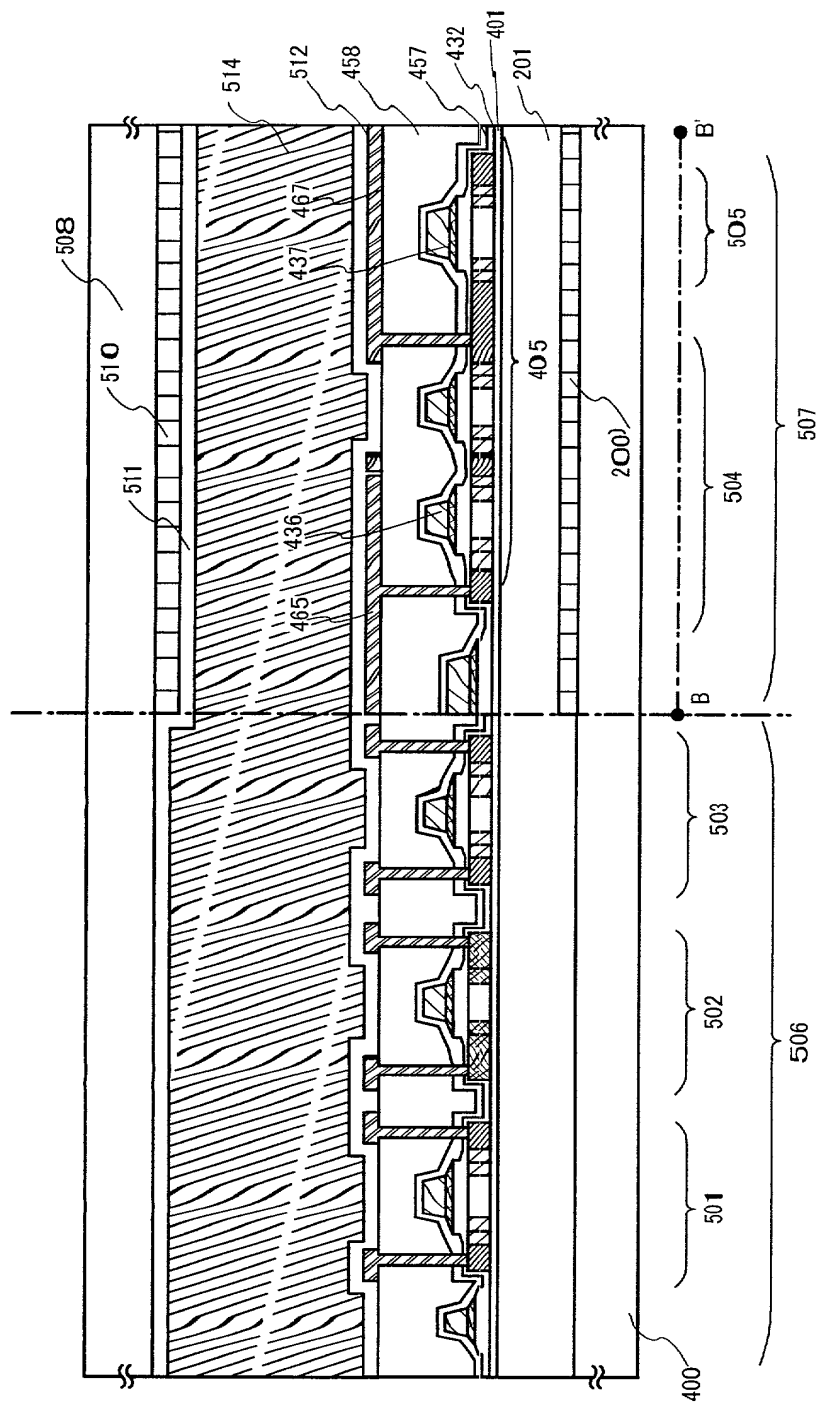


Fig. 7

09854120.0001001

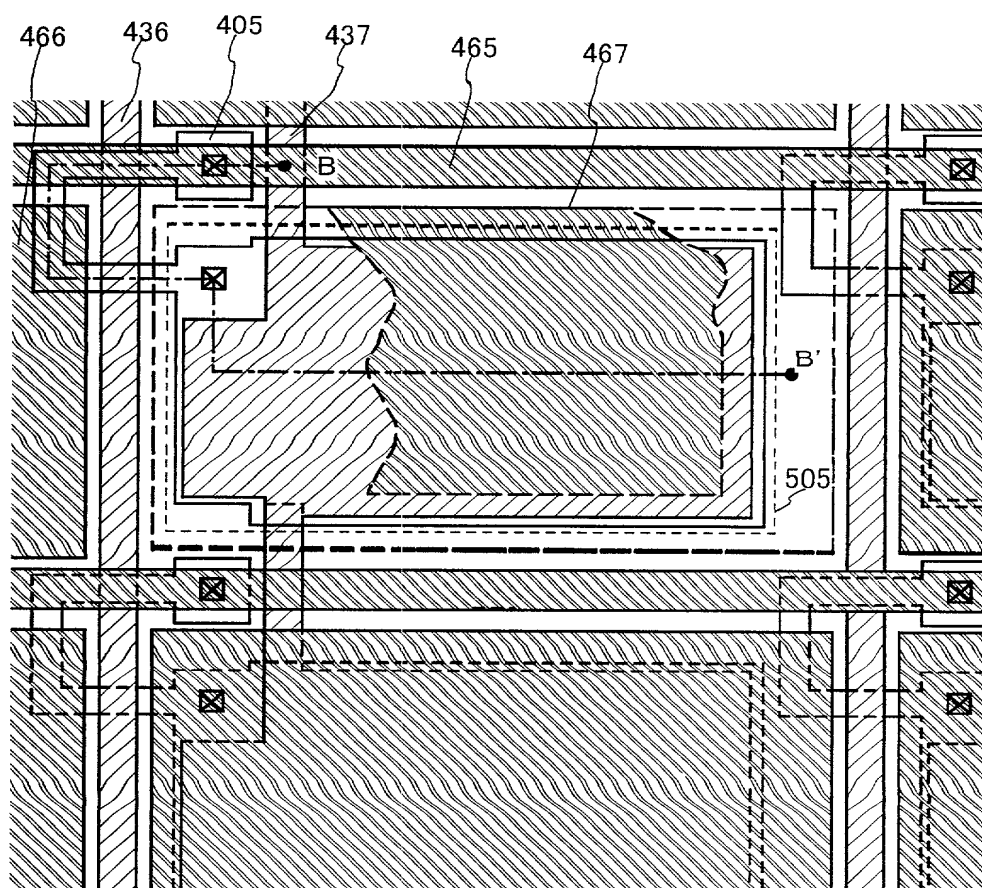


Fig. 8

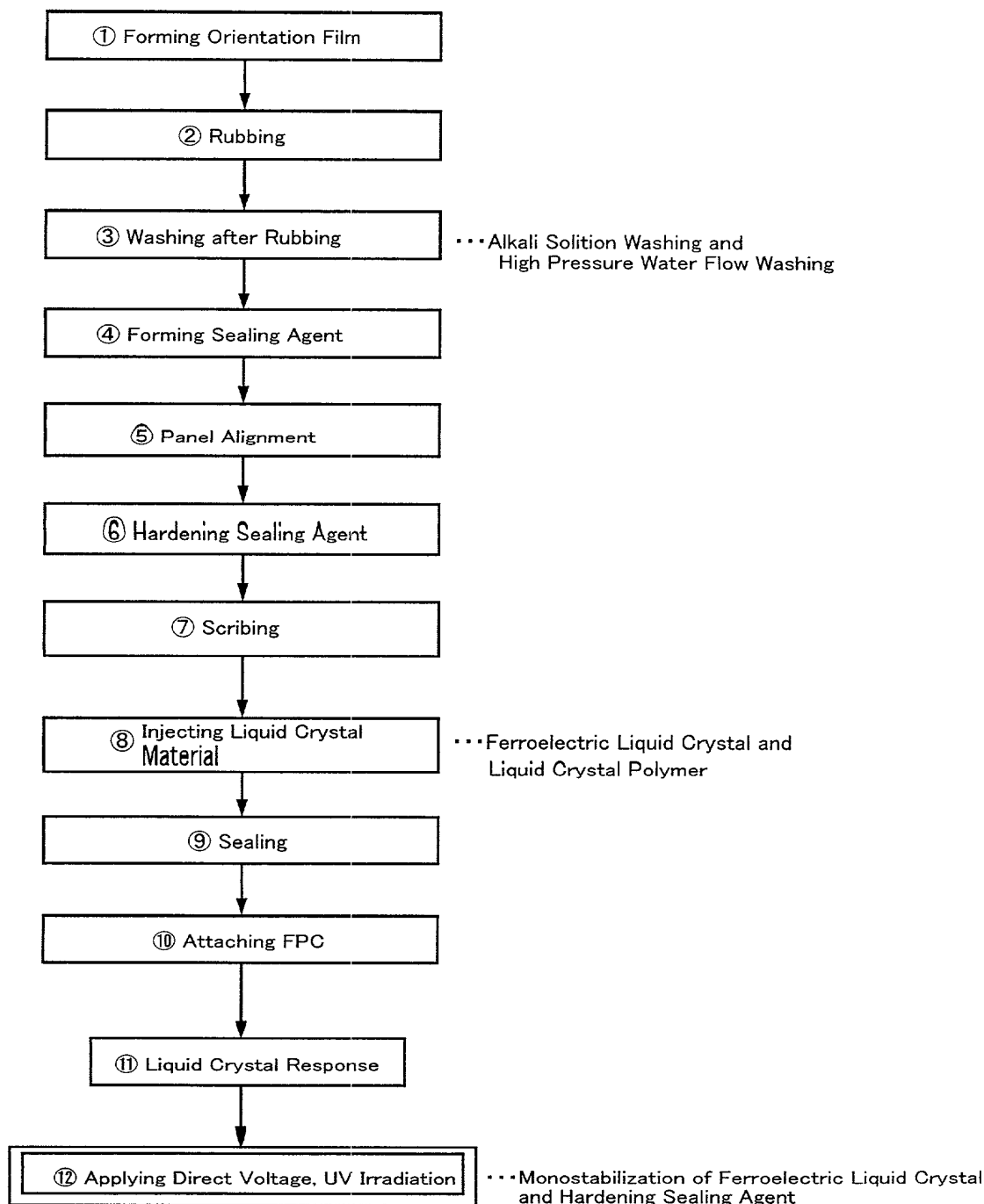


Fig. 9

09854120.051001

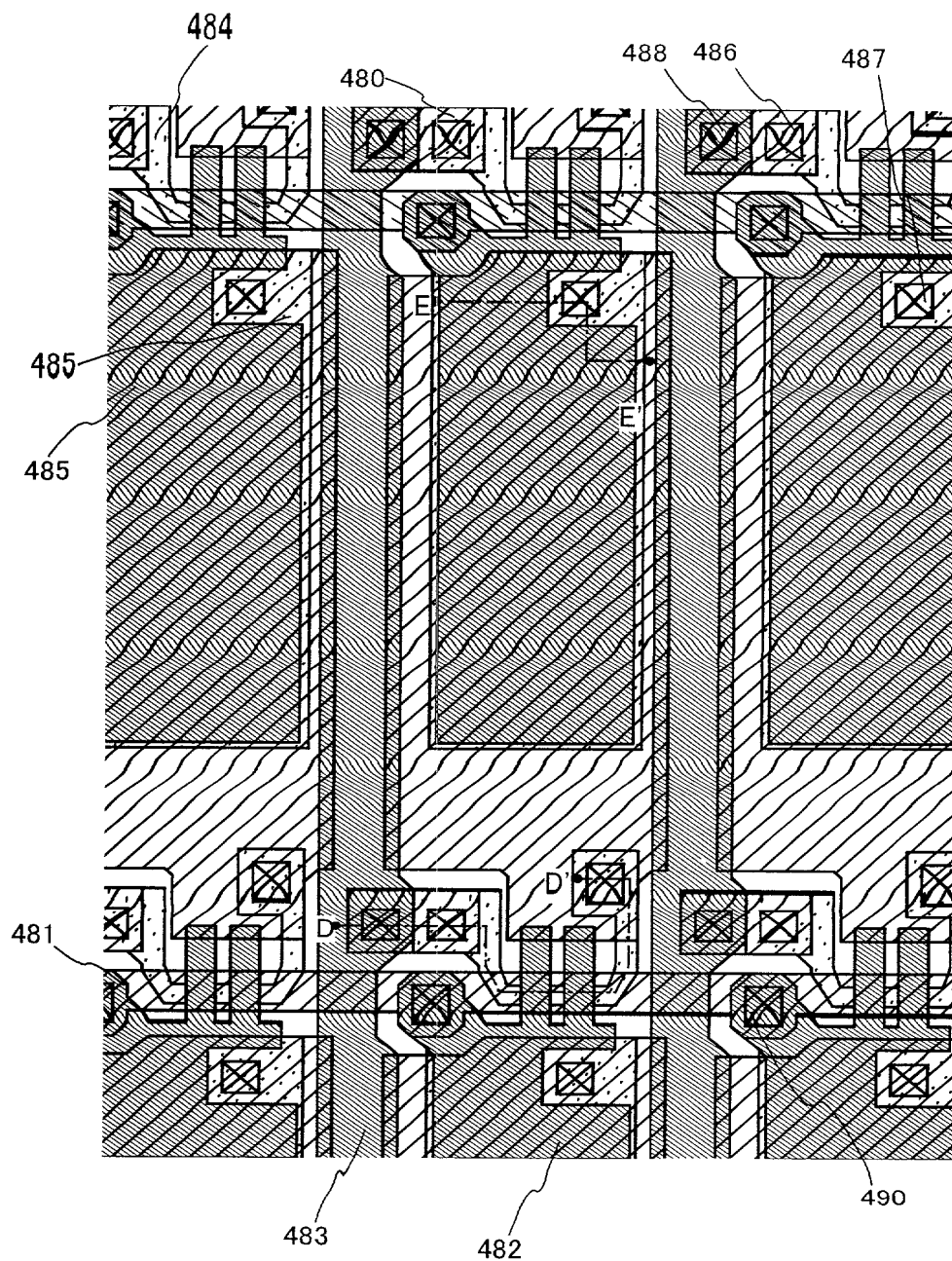


Fig. 12

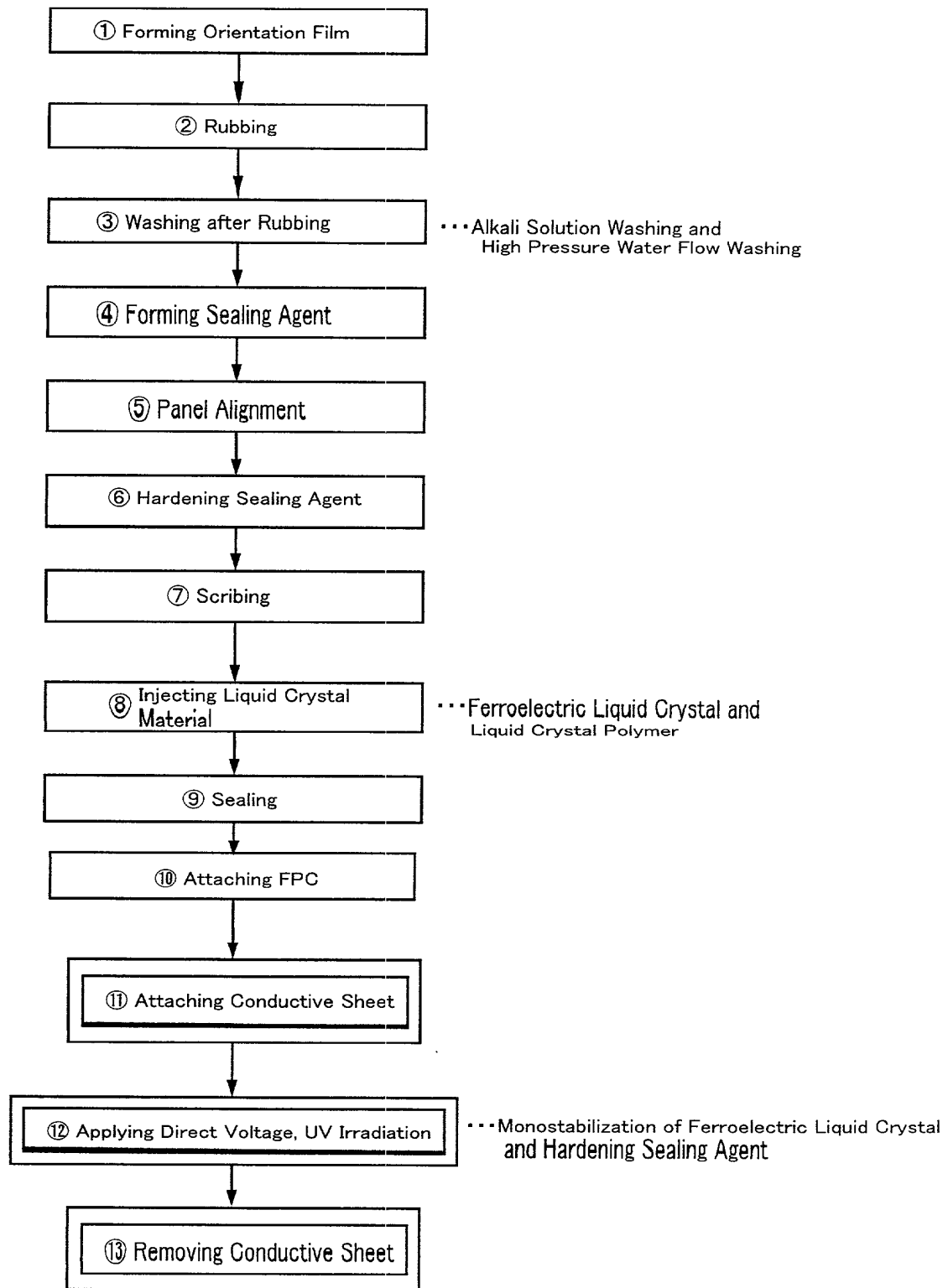


Fig. 13

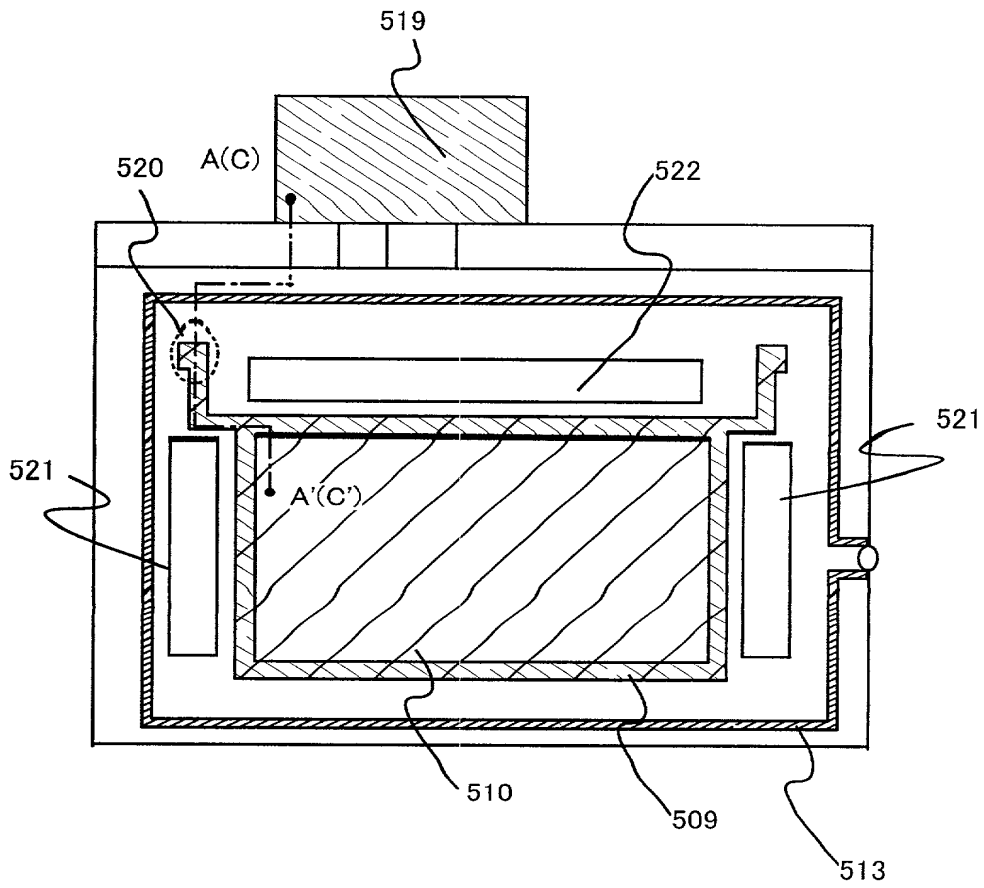


Fig. 14

Fi. 15C

Driving Circuit Portion Pixel Portion

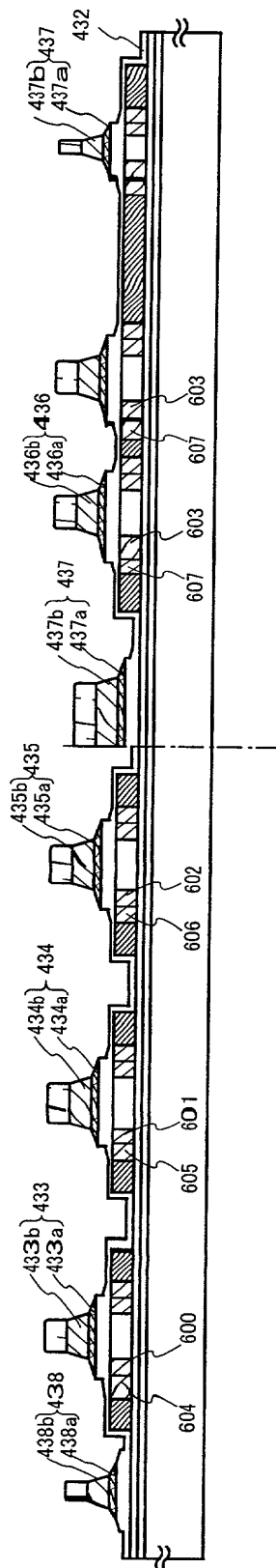


Fig. 16A

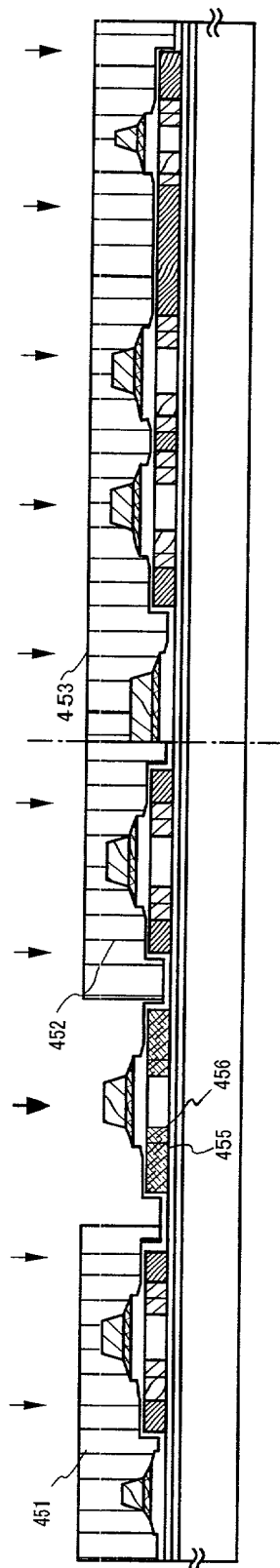


Fig. 16B

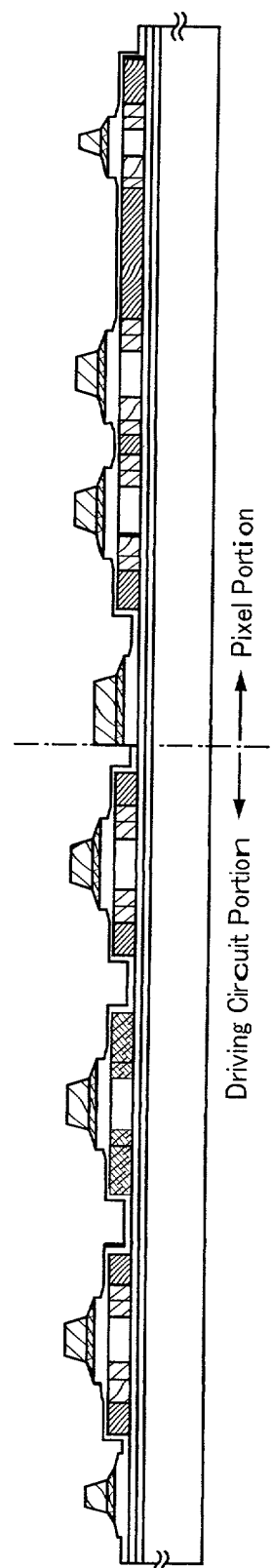


Fig. 16C

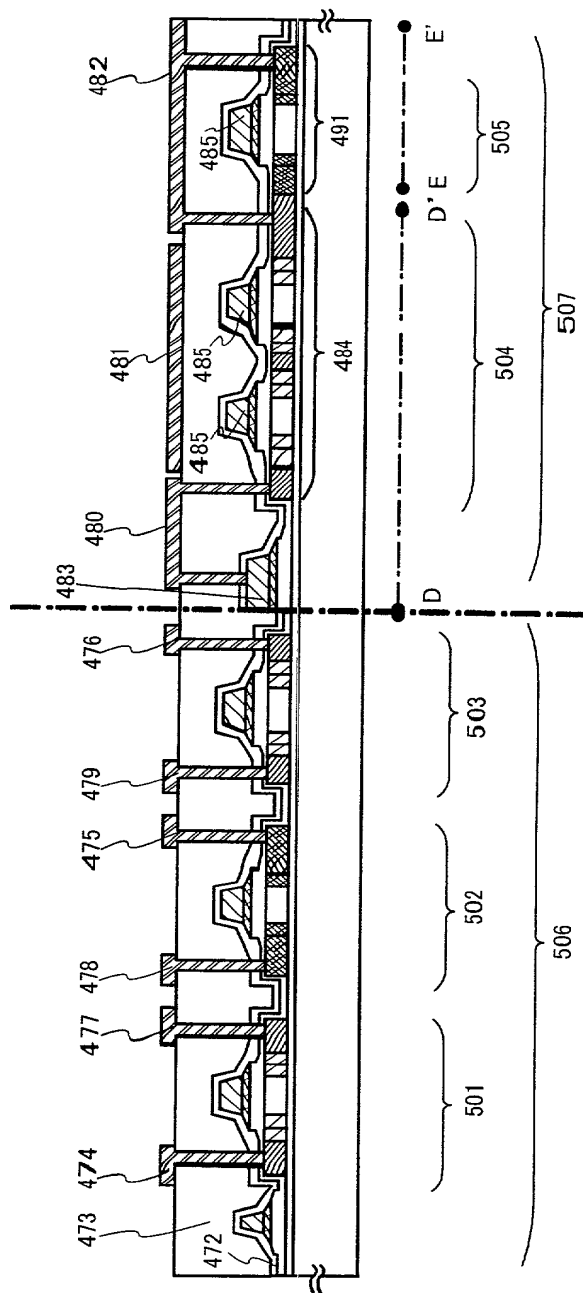


Fig. 18

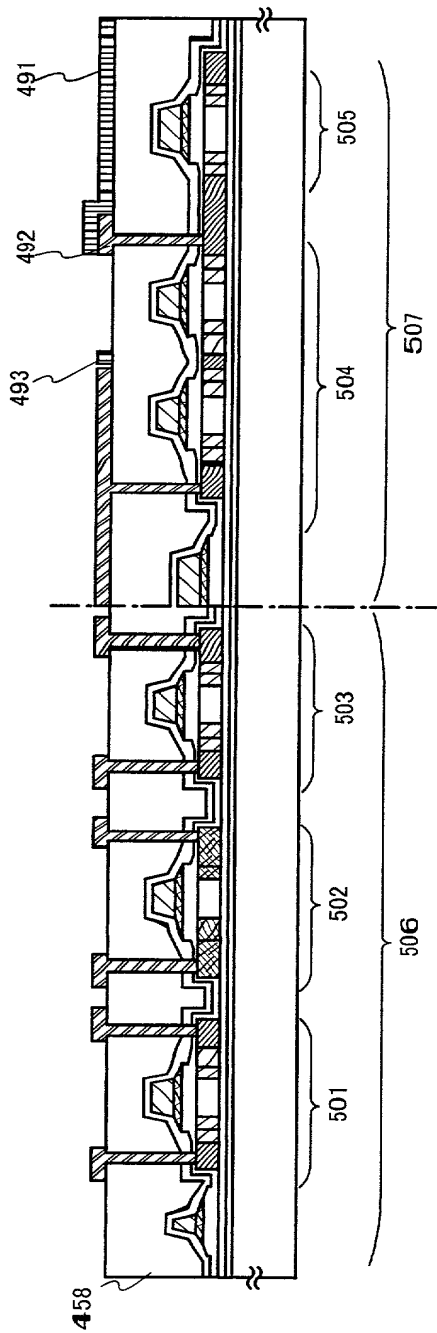


Fig. 19A

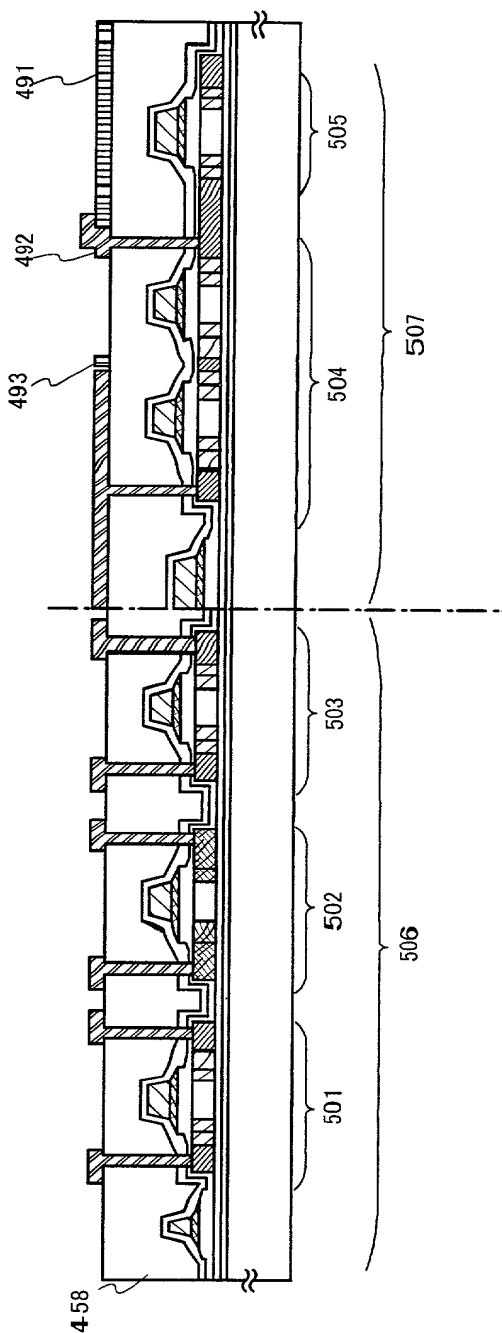


Fig. 19B

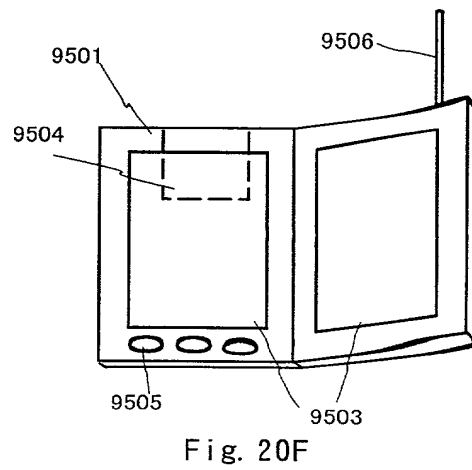
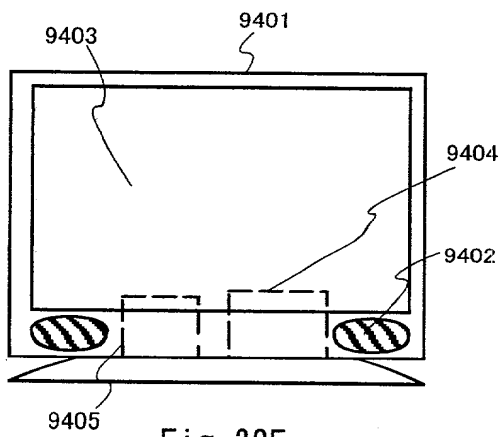
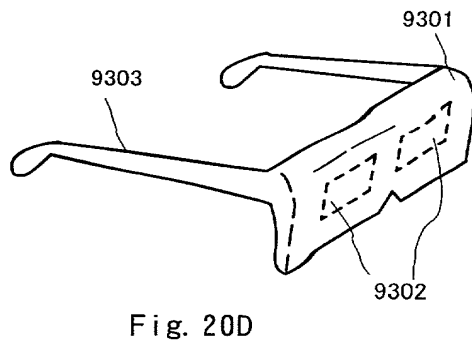
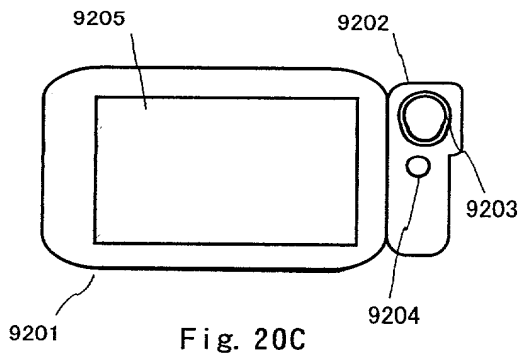
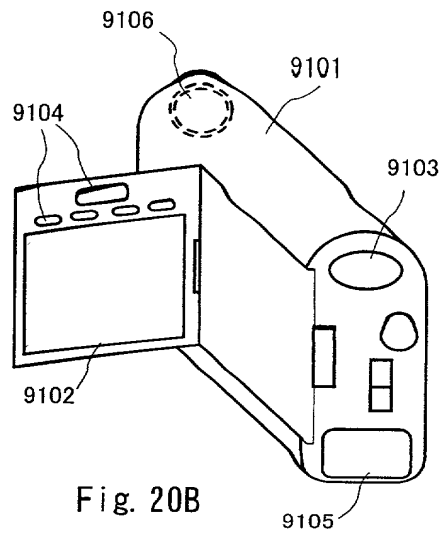
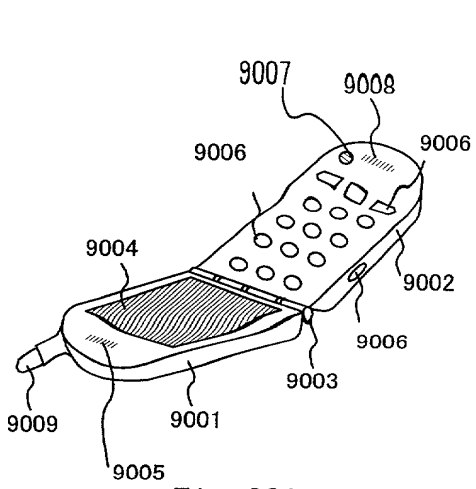


Fig. 21A

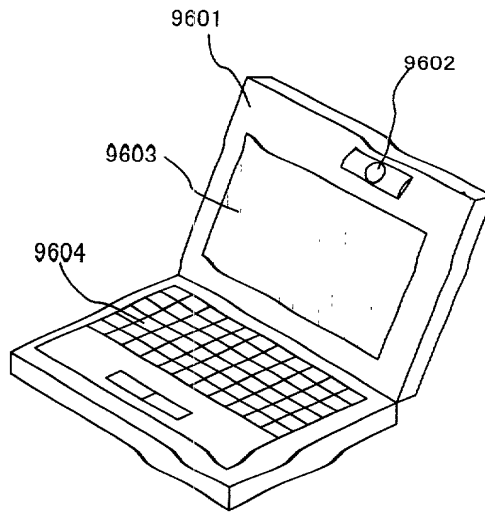


Fig. 21B

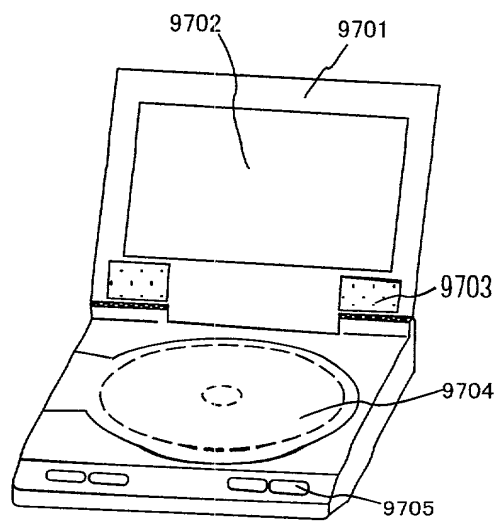
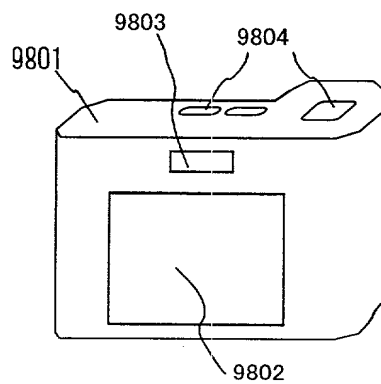


Fig. 21C



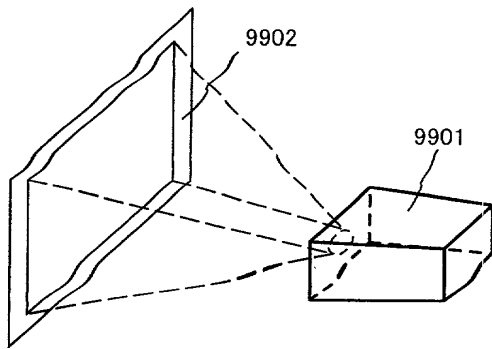


Fig. 22A

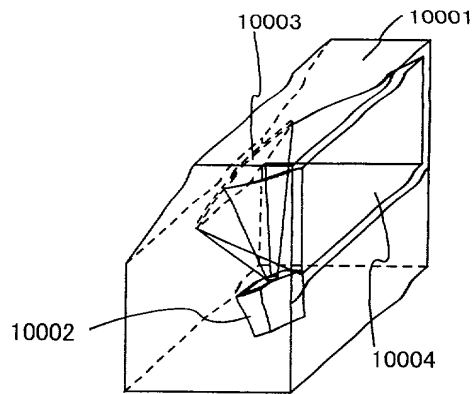


Fig. 22B

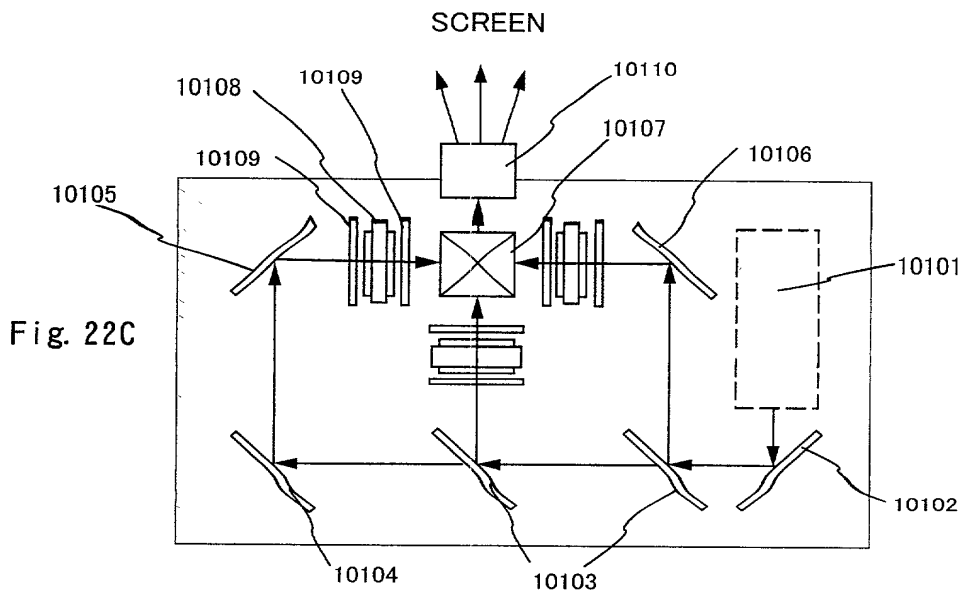


Fig. 22C

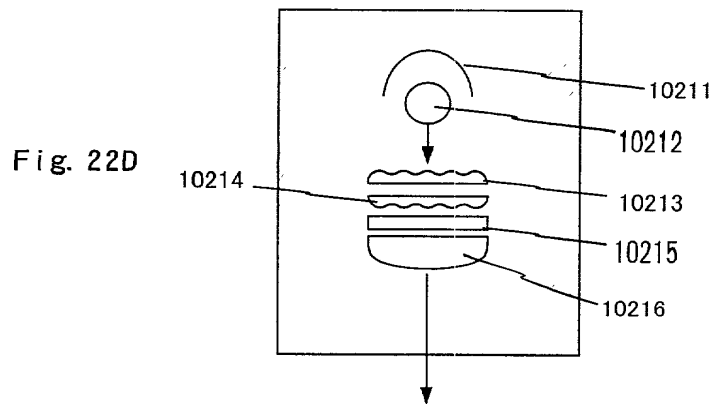


Fig. 22D

Fig. 23

019854120.061.001
T001590" 02T458610

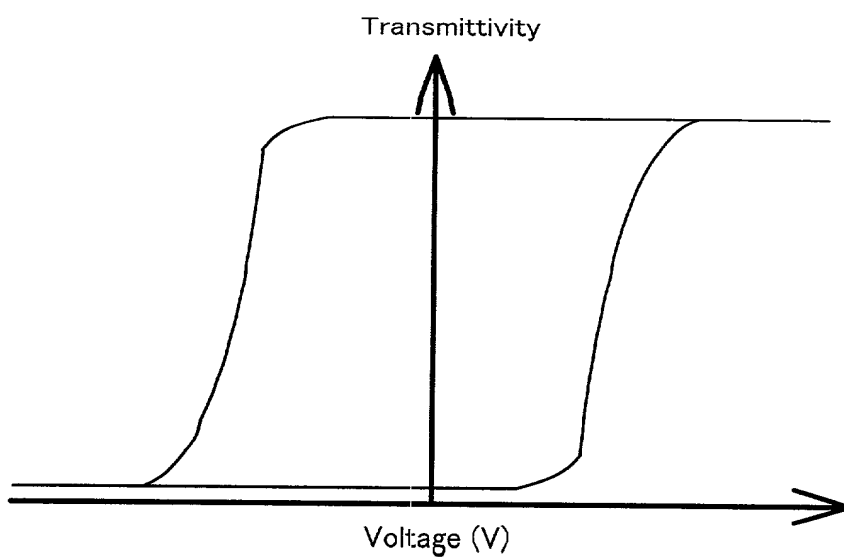


Fig. 24A

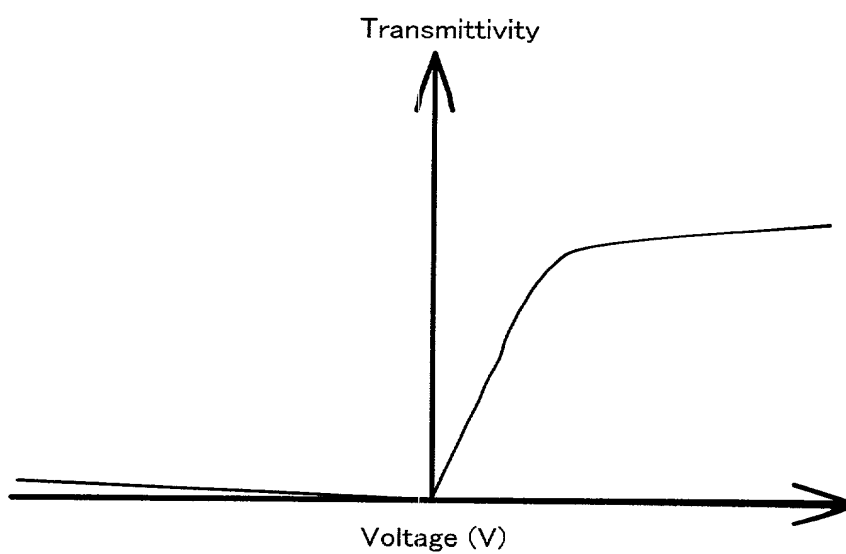


Fig. 24B

Fig. 25